

	2022		2		EECE273		01
							3-2-4
	(11:00 12:15) -LG [105] , (18:30 21:00) - 2 (3) [508-1]						
E-Mail	SHKANG@POSTECH.AC.KR			Homepage	SOC.POSTECH.AC.KR		
					054-279-2379		
Office Hours	Wed. 4:00 - 5:00 (LG 301)						
This course introduces students to the basic concepts of digital systems, including analysis and design. Both combinational and sequential logic will be covered. Students will gain experience with several levels of digital systems, from simple logic circuits to sequential system and hardware description language.							
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None							
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Attendance (10%) / Quiz (20%) / Assignment (20%) / Mid (20%) / Final Exam (30%)							
							ISBN
Handouts						0000	
Randy Katz and Gatetano Borriello, Contemporary Logic Design, 2nd Ed., 2005, Pearson/Prentice Hall							
01 Introduction#							
02 Combinational Logic Minimization#							
03 Working with Combinational Logic#							

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- 04 Combinational Logic Technology#
- 05 Case Studies in CL Design (Logic Function Unit, Adder)#
- 06 Sequential Logic Design#
- 07 Finite State Machines#
- 08 Midterm Exam#
- 09 Working with Finite State Machines##
- 10 Sequential Logic Technology#
- 11 Case Studies in Sequential Logic Design#
- 12 Number System#
- 13 CMOS design#
- 14 Hardware description language#
- 15 Design Methodology#
- 16 Final Exam

POSTECHx#
Zoom: <https://us02web.zoom.us/j/4587987246> (ID: 458 798 7246)